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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/053,818	01/22/2002	Yinon Degani	S1	4719

7590 04/13/2004
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EXAMINER

NGUYEN, VINH P

ART UNIT PAPER NUMBER

2829

DATE MAILED: 04/13/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/053,818

Applicant(s)

DEGANI ET AL.

Examiner

VINH P NGUYEN

Art Unit

2829

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 11/29/04.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-19 is/are pending in the application.
- 4a) Of the above claim(s) 1-5,10-12,17 and 18 is/are withdrawn from consideration.
- 5) ☒ Claim(s) 6-9 is/are allowed.
- 6) ☒ Claim(s) 13-16 and 19 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

1. Claims 13-16 and 19 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 13, it is unclear how the recited steps are interrelated and associated with “electrically comparing electrical elements on the IC wafer with electrical components in a reference circuit”.

In claim 19, it is unclear whether the fully functional test of the analog function of the analog IC chips at a frequency above 100 MHZ or the type of analog chips at a frequency above 100 MHZ.

The dependent claims not specifically address share the same indefiniteness as they depend from rejected base claims.

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 13 and 15 (insofar as understood) are rejected under 35 U.S.C. 102(b) as being anticipated by Chong (pat # 5,973,504).

As to claim 13, Chong discloses a programmable high density electronic testing device as shown in figure # 2 having step of providing a probe membrane (42) on a support (38) with an

opening, a plurality of probe contact array (43) located at the center of the probe membrane, reference electrical circuit components (44a,44b), mounting a wafer under test (12) on a platform (33), moving the center portion of the probe membrane so that the probe contact arrays (43) comes into contact with the wafer under test (12) and then pass electrical test signals between I/Os on the wafer under test and the reference circuit (44a,44b).

As to claim 15, it appears that the electrical components (44a,44b) is a part of an IC chip (45).

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Chong (pat # 5,973,504) in view of Sano (Pat # 5,703,494)

As to claim 19, Chong discloses a test apparatus for testing an IC wafer (12) with analog IC chips having step of probing the analog chips with an array of test contacts (43), performing a fully functional test (30) of the analog IC chips through the probe card (14) and the test controller (20) as shown in figure 1. However, Chong does not mention about the testing chips at

a frequency above 100 MHZ. However, Sano teach that it would have been well known to test the chip on the wafer from 10 Mhz to about 100 MHZ or higher than this range (see column 1, lines 12-18). It would have been well known for one of ordinary skill in the art to provide the teaching of Sano by testing chips at frequency above 100 MHZ to the device of Chong since now a day, the chips are fabricated at high speed (above 100 MHZ).

6. Claims 6-9 are allowable since the prior art does not disclosean IC test apparatus having at least one reference circuit component such as an LC circuit attached to the polymer membrane and located adjacent to the probe contact array .

7. Applicant's arguments with respect to claims 6-9,13-16 and 19 filed on 01/29/04 have been considered but are moot in view of the new ground(s) of rejection.


8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VINH P NGUYEN whose telephone number is (521)272-1964. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


VINH P NGUYEN
Primary Examiner
Art Unit 2829
04/05/04